Patent

Attorney Docket No.: Intel 2207/10122

Assignee: Intel Corporation

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS

Sailesh B. KOTTAPALLI

SERIAL NO.

09/753,764

FILED

December 29, 2000

FOR

METHOD AND APPARATUS FOR REDUCING COMPONENTS NECESSARY FOR INSTRUCTION POINTER GENERATION IN A SIMULTANEOUS

MULTITHREADED PROCESSOR

GROUP ART UNIT

2183

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EXAMINER

David J. HUISMAN

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M/S: NON-FEE AMENDMENTS

Commissioner for Patents

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CERTIFICATE OF MAILING

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Dated: June 18, 2004

Cianatura

Stephen T. Neal

AMENDMENT AND RESPONSE

Dear Sir:

The following were sent in response to the office action mailed on March 19, 2004.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Amendments to Specification begin on page 8 of this paper.

Remarks/Arguments begin on page 11 of this paper.